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In re Patent Application of)

Keiichi FUJIMOTO et al)

Serial No. 09/964,480)

Filed: September 28, 2001)

For: SEMICONDUCTOR)

INTEGRATED CIRCUIT)

TESTING SYSTEM AND)

METHOD)

Attention: Applications Branch TC 2800 MAIL ROOM

CERTIFICATE OF MAILING

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Sir:

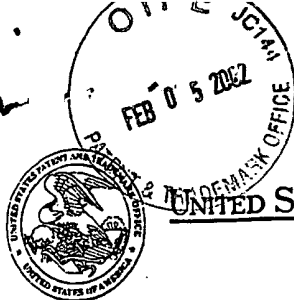
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Patent Office error as shown in red on the attached copy.

In view of the above, it is requested that a Corrected Filing Receipt be issued.

Respectfully submitted,

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/964,480	09/28/2001	2858	710	740819-653	9	12	2

CONFIRMATION NO. 2785

FILING RECEIPT



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22204
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SUITE 800
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Date Mailed: 10/30/2001

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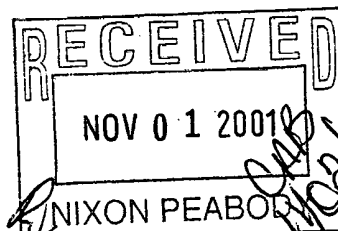
Title

Semiconductor integrated circuit testing system and method

Preliminary Class

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